

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	740	712/228.ccls.	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L2	183	L1 and conditional	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L3	39	L1 and (conditional near5 (flag or bit or status))	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L4	0	L1 and ((conditional near5 (flag or bit or status)) same context\$4)	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L5	25	L1 and ((condition\$3 near5 (flag or bit or status)) same context\$4)	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L6	1	("20010010075").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/04/24 16:48
L7	1	L6 and dirty	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L8	1	L1 and "extended addressing register"	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L9	19	("context switch") same "dirty bit"	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L10	5	((("context switch") same "dirty bit") and stack	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L11	71	(stack adj pointer) same "context switch"	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L12	476	stack same "context switch"	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L13	62	L12 same (flag or status or "dirty bit")	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L14	54	L1 and (stack near4 context)	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L15	77	("context switch") near3 stack	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L16	86	((context or process) adj switch) near3 stack	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L17	4137	"context switch"	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L18	50	L1 and dirty	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48

L19	2	"two pc registers"	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L20	154	"two program counter"	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L21	3	"extended addressable space"	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L22	2	((("6205467") or ("6367005")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/04/24 16:48
L23	0	("EP942357A").PN.	US-PGPUB; USPAT; USOCR; EPO	OR	OFF	2008/04/24 16:48
L24	4	((("6014739") or ("5935240") or ("5903919") or ("6199155")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/04/24 16:48
L25	163	(save near5 "program counter") same (flag or bit)	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L26	6	(save near5 "program counter") same (flag or bit) same context	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L27	4	(save near5 "program counter") near4 (select \$5)	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L28	1	("6877084").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/04/24 16:48
L29	2	("6877084").URPN.	USPAT	AND	ON	2008/04/24 16:48
L30	5	(extended near2 register?) same "context switch"	USPAT	AND	ON	2008/04/24 16:48
L31	512	intel.as. and "386"	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L32	1	L31 and ("386" with "286") same compat\$6	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L33	0	L31 and (backwards near3 compatability)	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L34	137	intel.as. and (backwards adj compatibility)	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L35	11	L31 and (backwards near3 compatibility)	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L36	2	L35 and context	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48

L37	0	L35 and emulation	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L38	83	L31 and emulat\$3	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L39	97	emulation same (context adj switch\$3)	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L40	12	L38 and "context switch"	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L41	41	L38 and context	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L42	122	emulat\$3 same "286"	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L43	389	"context switch" and (("8 bit" with "16 bit"))	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L44	17	L43 and intel.as.	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L45	16	L42 same mode	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L46	0	L45 and intel.as.	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L47	0	L42 same (context adj switch\$3)	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L48	6	L42 and (context adj switch\$3)	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L49	295	(context switch\$3) same "instruction set"	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L50	43	L49 and dirty	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L51	40	(save with register) same ((extended or expanded) with mode)	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L52	1	("5838984").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/04/24 16:48

L53	50	("4044338" "4050094" "4109311" "4385352" "4453212" "4679140" "4807115" "4835734" "4858105" "4926322" "4928223" "4972338" "5053631" "5058048" "5109334" "5125087" "5129067" "5136697" "5226126" "5226130" "5226132" "5274834" "5293592" "5303358" "5321836" "5375213" "5438668" "5471593" "5481684" "5560032" "5561784" "5630149" "5651125" "5655125" "5689672" "5758116" "5768574" "5809273" "5822778" "5838984" "5848284" "5903919" "5905900" "5935240" "6014739" "6145049" "6157996" "6418527").PN. OR ("6877084").URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2008/04/24 16:48
L54	5	L53 and "context switch"	US-PGPUB; USPAT; USOCR	AND	ON	2008/04/24 16:48
L55	740	712/228.ccls.	US-PGPUB; USPAT	AND	ON	2008/04/24 16:48
L56	2	((("4445173") or ("5325494")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/04/24 16:48
L57	1	"extended addressing register"	US-PGPUB	AND	ON	2008/04/24 16:54
L58	335	(sav\$3) same variable same register?	US-PGPUB	AND	ON	2008/04/24 16:54
L59	38	(sav\$3) same variable adj2 register?	US-PGPUB	AND	ON	2008/04/24 16:55

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